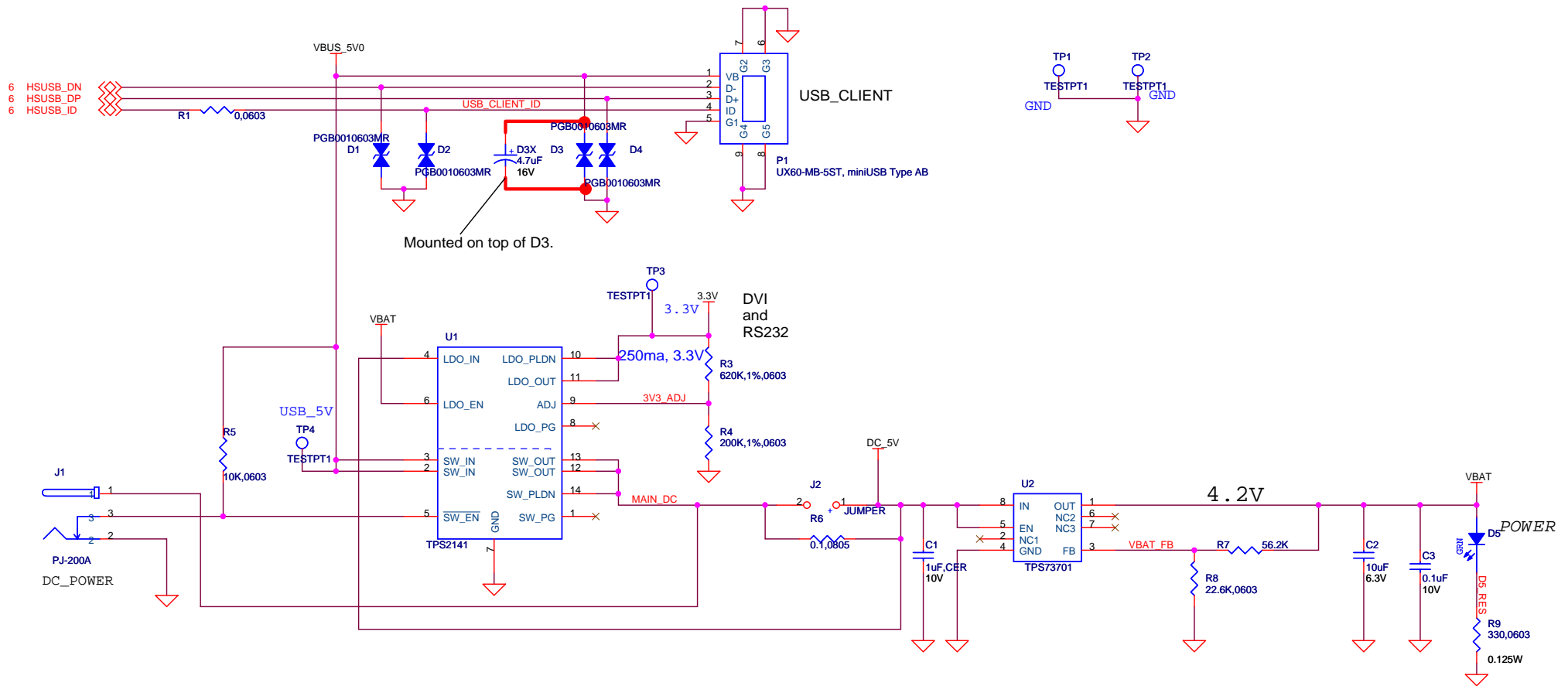


REV	Description	DATE	BY
B1	1. Added 1K resistor to the GPMC_WAIT0 line. 2. Change power connector symbol to fix the swapped pins. 3. Add plated through mounting holes to P10 and provide soldering pad on the back side. 4. Fix hole size on P8 and make them plated through with a soldering pad on the back side. 5. Changed C101 to 47uf, 25V. 6. Removed TP15, TP16, TP17, and TP18. 7. USER0 and USER1 LEDs are wired wrong. Corrected net naming error on schematic. 8. Added 10K pullup to USB1HS_nCS signal. 9. Deleted P3, R53, R24, and R25.	4/8/08	GC
B2	1. Removed the USB host components from this assembly as the USB host on this layout is not reliable.	6/3/08	GC
B3	1. Added capacitors back in to reestablish noise margins.	6/18/08	GC
B4	1. Added 4.7uf across D3 to improve noise levels on OTG VBus. Some hubs would not work without this capacitor.	6/25/08	GC

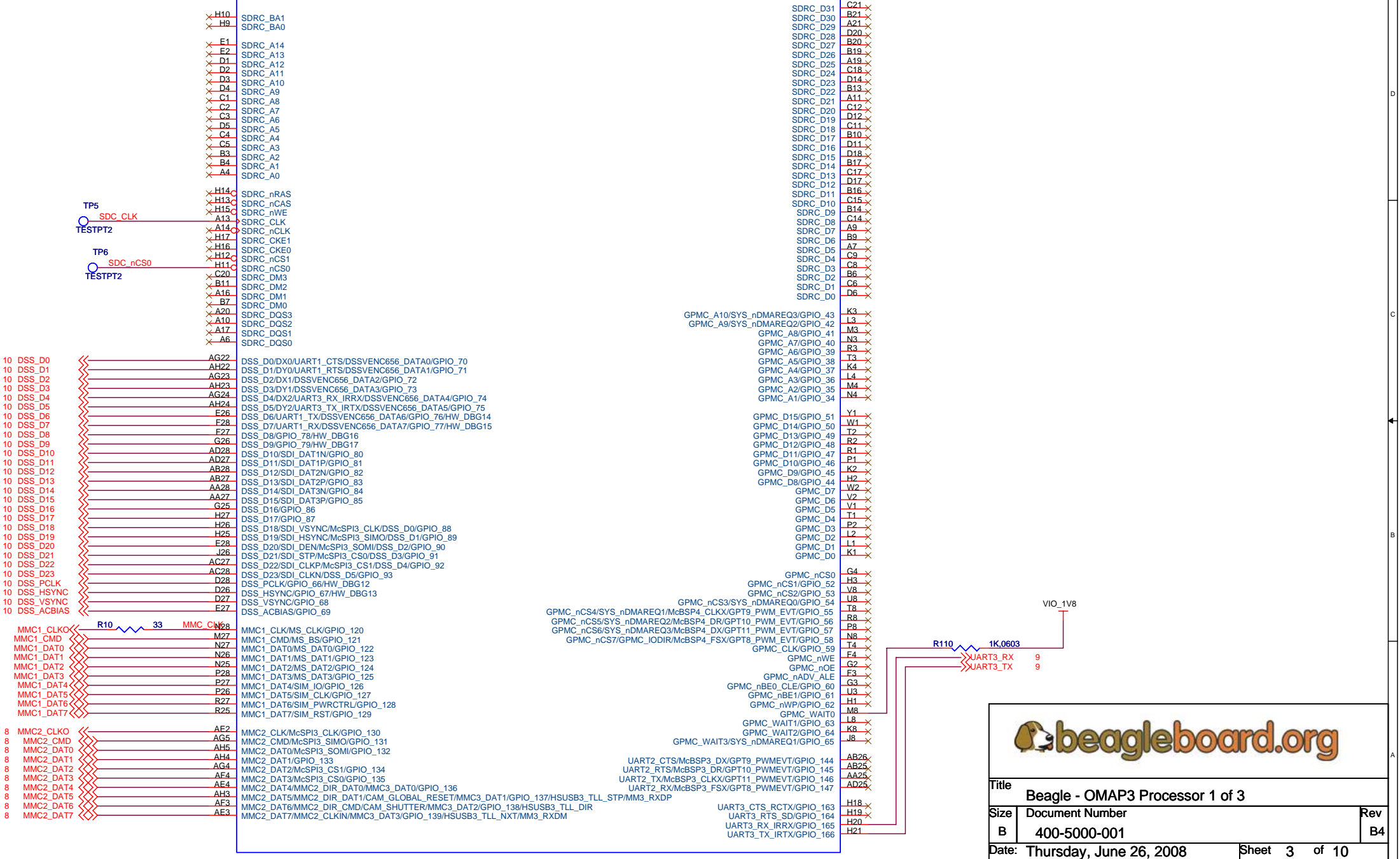
CONTENTS	
PAGE NO.	SCHEMATIC PAGE
1	COVER PAGE
2	USB OTG CONNECTOR AND MAIN POWER
3	OMAP3 1 OF 3
4	OMAP3 2 OF 3, JTAG, SWITCHES, LEDS, SVIDEO
5	OMAP3 3 OF 3
6	TWL4030 1 of 2, AUDIO JACKS, LED, 26MHZ, 32KHZ
7	TWL4030 2 of 2
8	EXPANSION
9	SD/MMC, SERIAL HEADER
10	DVI-D




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Title		
Beagle - USB OTG and Power		
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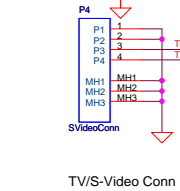




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Beagle - OMAP3 Processor 1 of 3		
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14 PIN JTAG INTERFACE

EMULATOR PULL UP/DOWN RESISTORS



Insures that the DVI-D is powered down at powerup.

Resistor are loaded based upon POP memory type. Default for this revision is NAND.

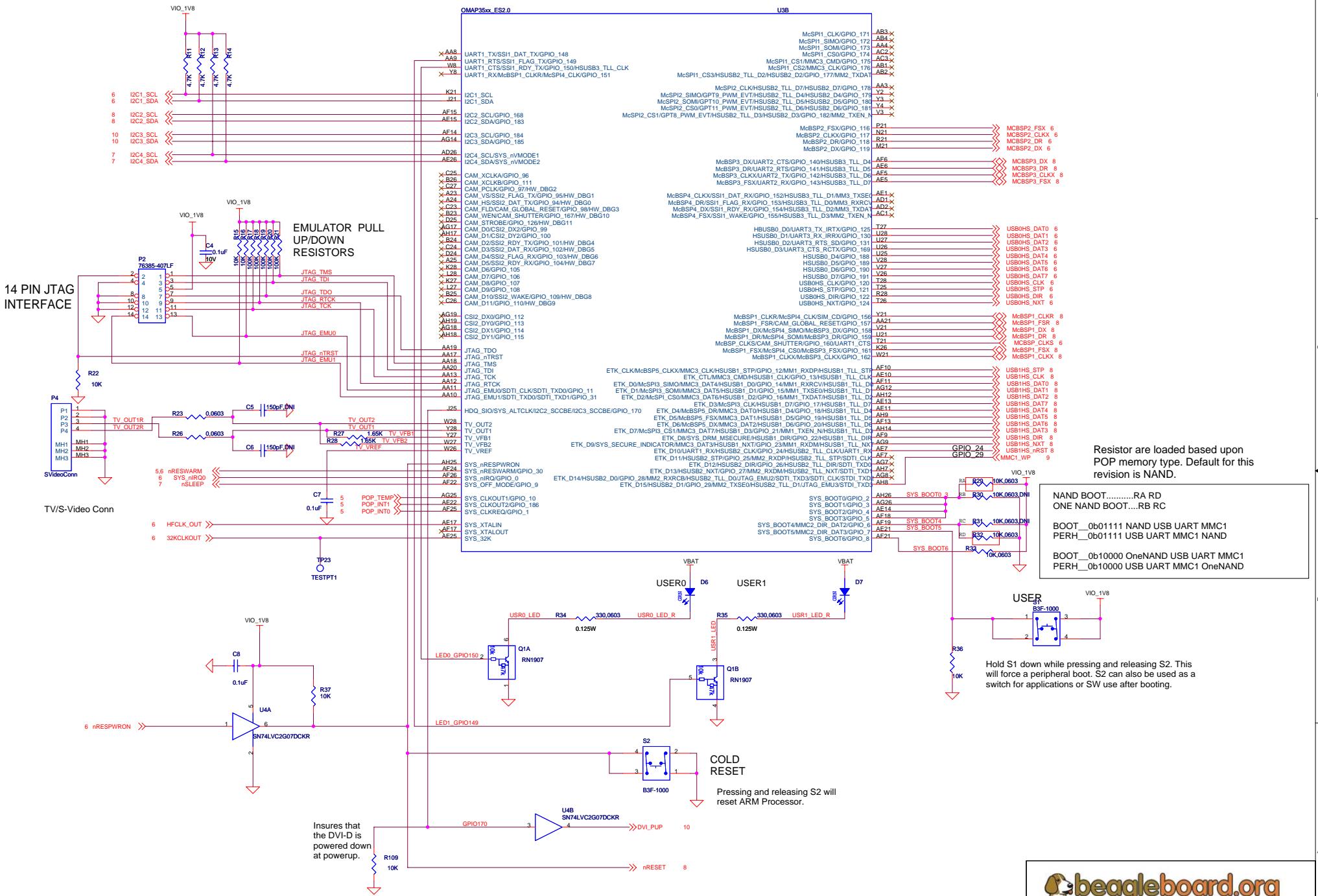
NAND BOOT.....RA RD
ONE NAND BOOT....RB RC

BOOT__0b01111 NAND USB UART MMC1
PERH__0b01111 USB UART MMC1 NAND

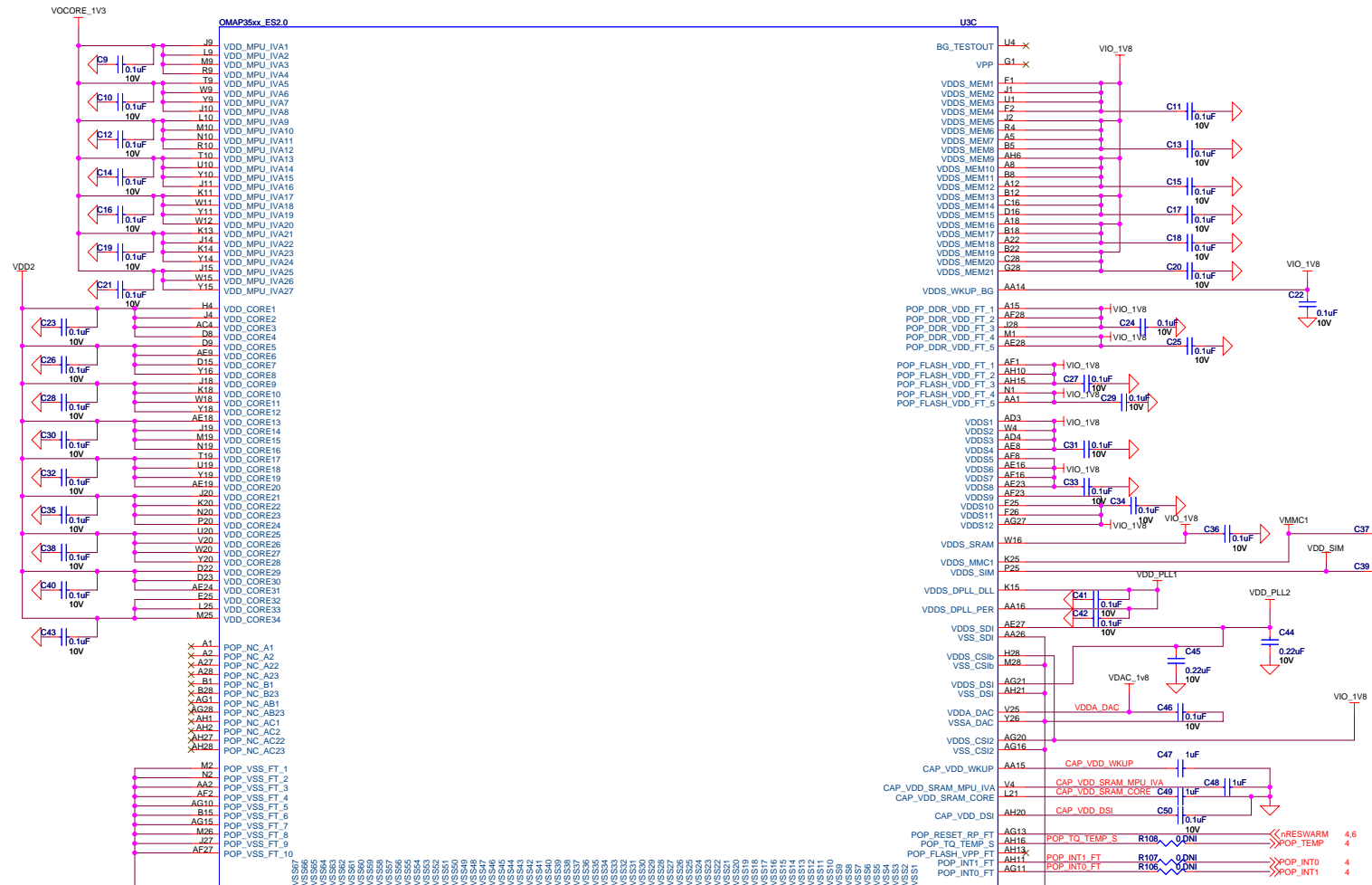
BOOT__0b10000 OneNAND USB UART MMC1
PERH__0b10000 USB UART MMC1 OneNAND

Hold S1 down while pressing and releasing S2. This will force a peripheral boot. S2 can also be used as switch for applications or SW use after booting.

Pressing and releasing S2 will reset ARM Processor.

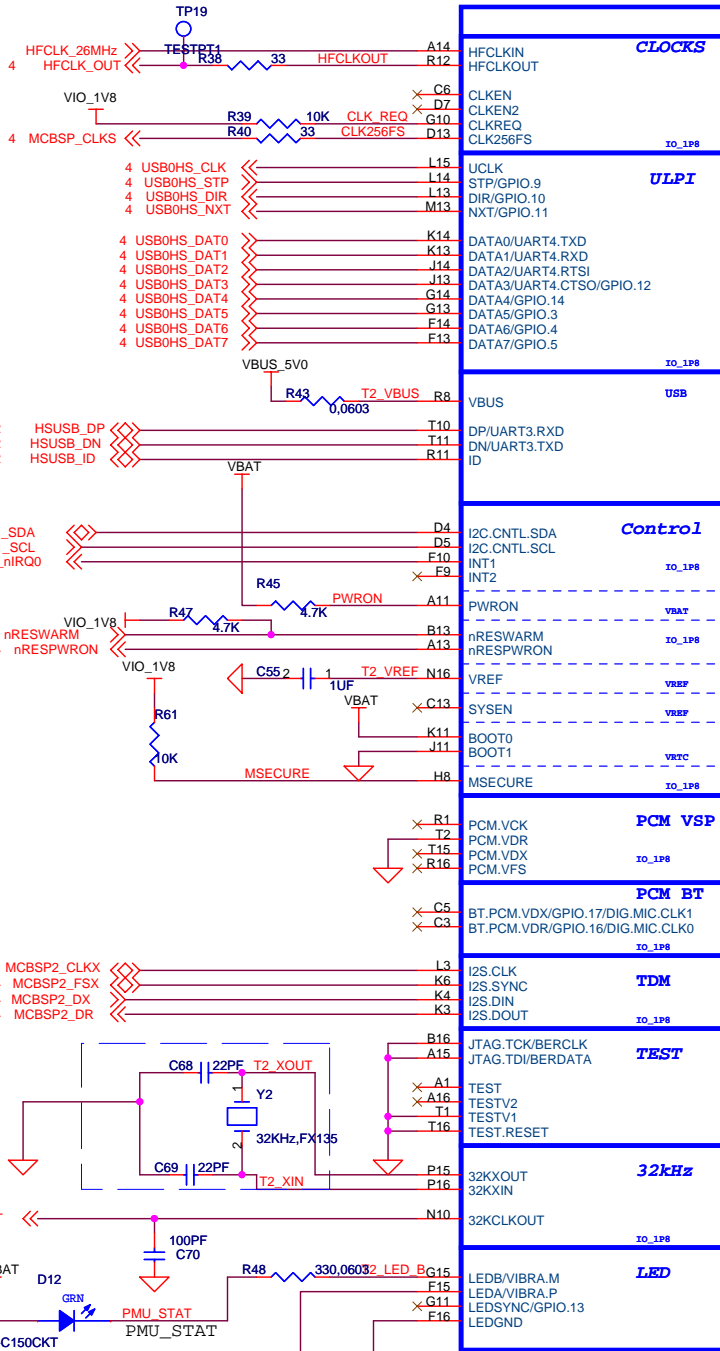


Title Beagle - OMAP3 Processor 2 of 3, LEDs, Switches		
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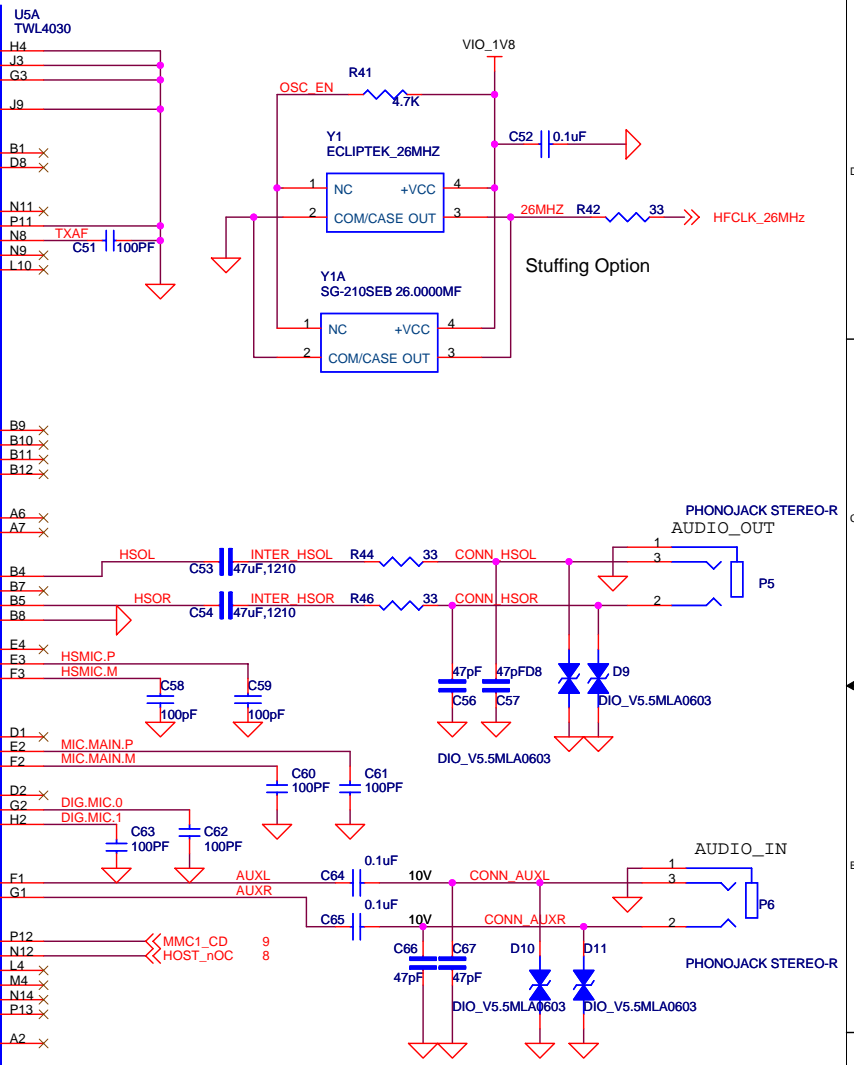
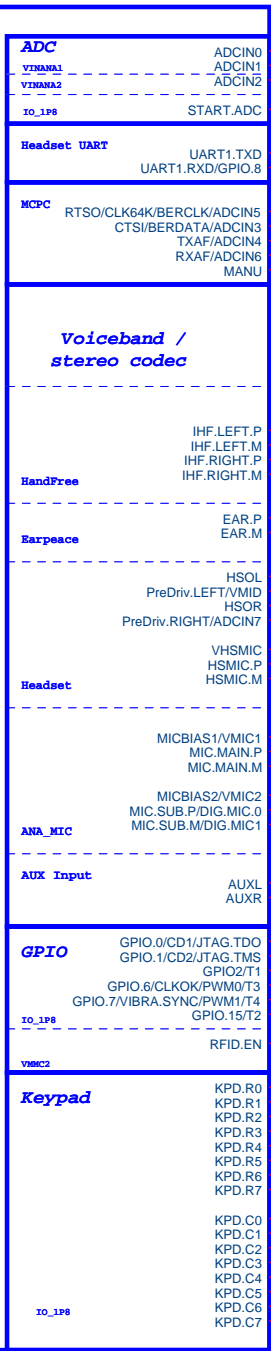
- ✕ A1 POP_NC_A1
- ✕ A2 POP_NC_A2
- ✕ A28 POP_NC_A23
- ✕ B1 POP_NC_B1
- ✕ B28 POP_NC_B23
- ✕ A31 POP_NC_AB1
- ✕ A328 POP_NC_AB23
- ✕ AH1 POP_NC_AC1
- ✕ AH2 POP_NC_AC2
- ✕ AH27 POP_NC_AC22
- ✕ AH28 POP_NC_AC23
- M2 POP_VSS_FT_1
- N2 POP_VSS_FT_2
- AA2 POP_VSS_FT_3
- AF2 POP_VSS_FT_4
- AG10 POP_VSS_FT_5
- B15 POP_VSS_FT_6
- AG15 POP_VSS_FT_7
- M26 POP_VSS_FT_8
- J27 POP_VSS_FT_9
- AF27 POP_VSS_FT_10

- VS87
- VS86
- AC246
- VS85
- VS84
- VS83
- VS82
- VS81
- VS80
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- VS7
- VS6
- VS5
- VS4
- VS3
- VS2
- VS1



TWL4030 Triton2

PART A



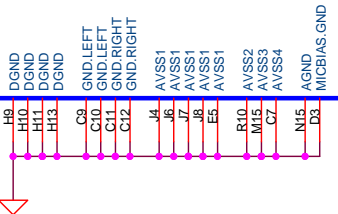
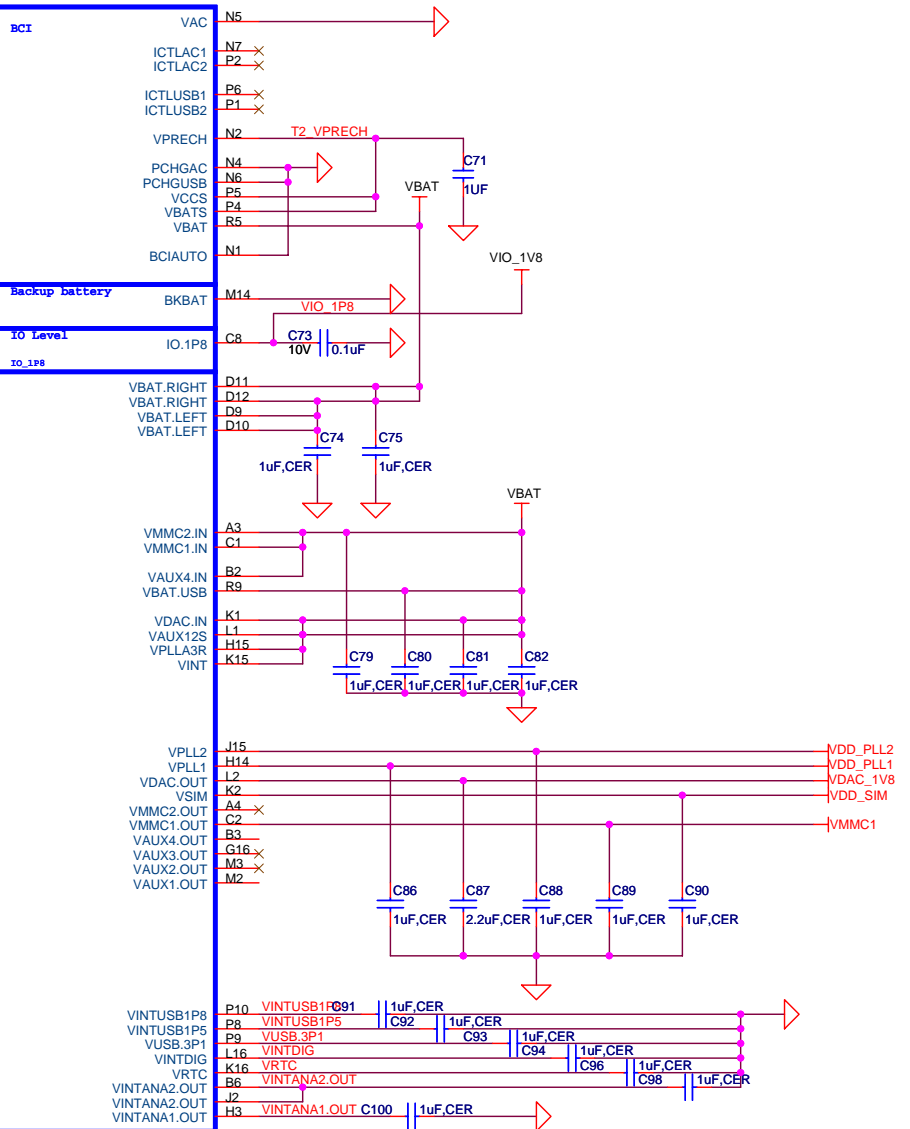
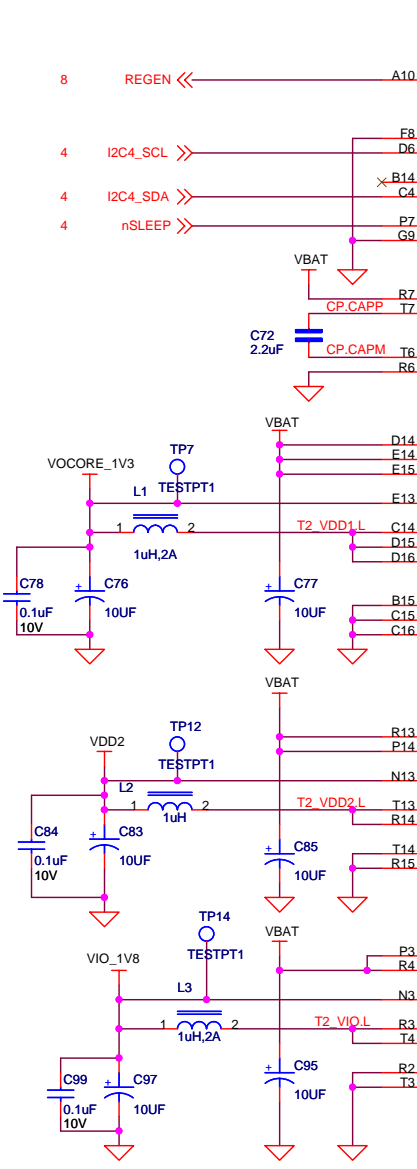
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Part B Power

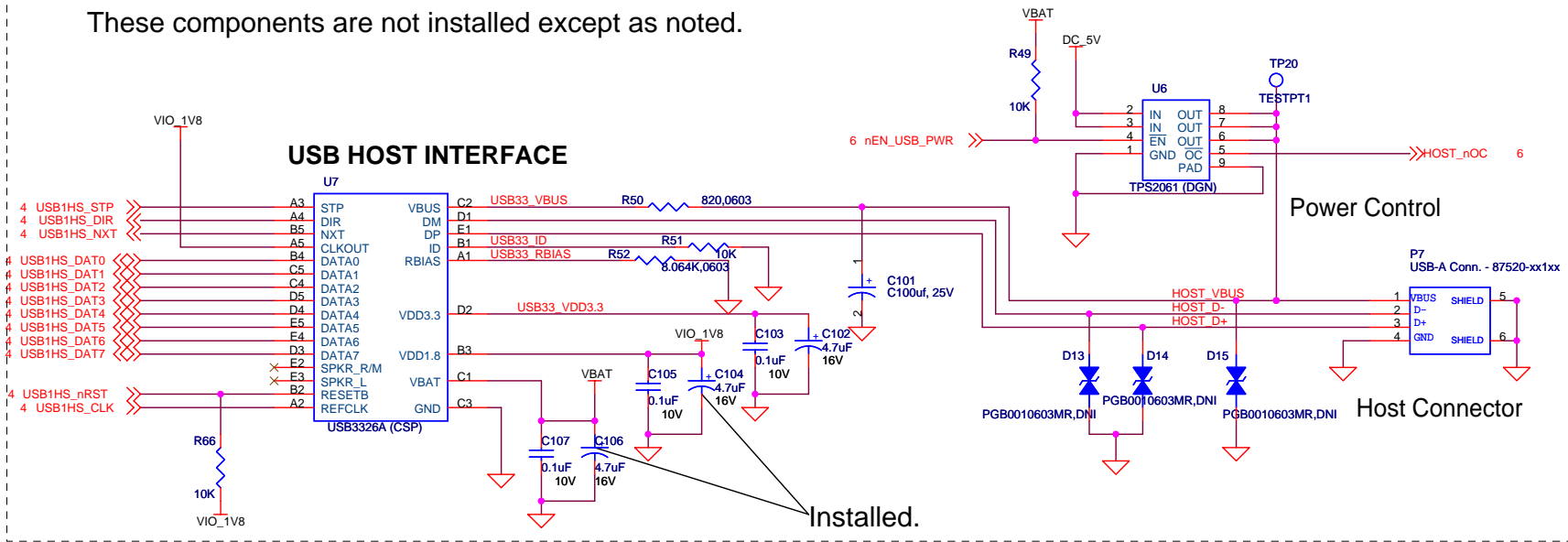
Domain	Type	Voltage	Current

External			
VDD1	SMPS	0.6V to 1.45V	1100mA
VDD2	SMPS	0.6V to 1.45V/1.5V	600mA
VIO	SMPS	1.8V /1.85V	600mA
VBUS	CP	4.8V	100mA
VAUX1	LDO	2.5V/2.8V/3.0V	200mA
VAUX2	LDO	1.0V/1.2V/1.5V/1.8V/2.5V/2.8V	100mA
VAUX3	LDO	1.5V/1.8V/2.5V/2.8V	200mA
VAUX4	LDO	0.7V/1.0V/1.2V/1.5V/1.8V/2.5V/2.8V	100mA
VMMC1	LDO	1.85V/2.85V/3.0V/3.15V	220mA
VMMC2	LDO	1.85V/2.6V/2.85V/3.0V/3.15V	100mA
VMIC1	LDO	1.8V	10mA
VMIC2	LDO	1.8V	10mA
VSIM	LDO	1.8V/2.8V/3.0V	50mA
VDAC	LDO	1.2V/1.3V/1.8V	65mA
VPLL1	LDO	1.0V/1.2V/1.3V/1.8V	40mA
VPLL2	LDO	0.7V/1.0V/1.2V/1.3V/1.8V	60mA
Internal			
VUSB	LDO	3.1V	15mA
VUSB_1P5	LDO	1.5V	30mA
VUSB_1P8	LDO	1.8V	30mA
VINTDIG	LDO	1.5V	50mA
VINANA1	LDO	1.5V	50mA
VINANA2	LDO	2.5V/2.75V	250mA
VRTC	LDO	1.5V	

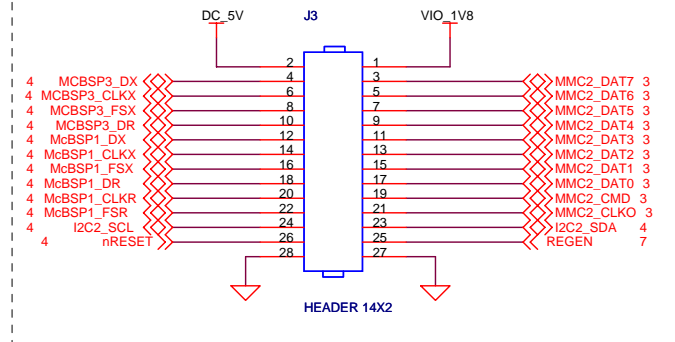


Title		
Beagle - Power Management 2 of 2		
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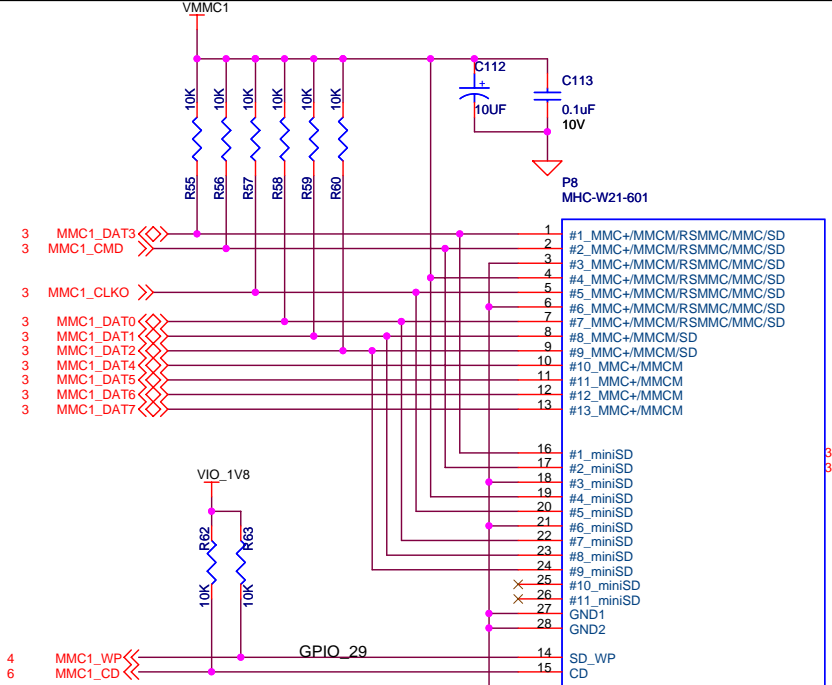
These components are not installed except as noted.



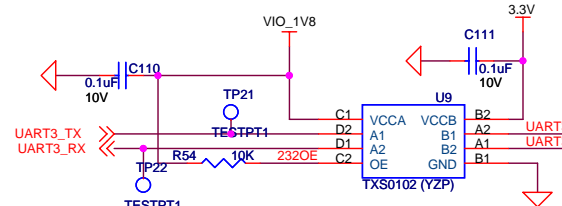
Expansion Connector



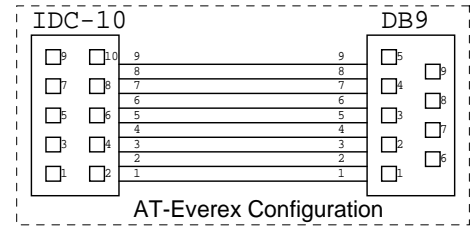
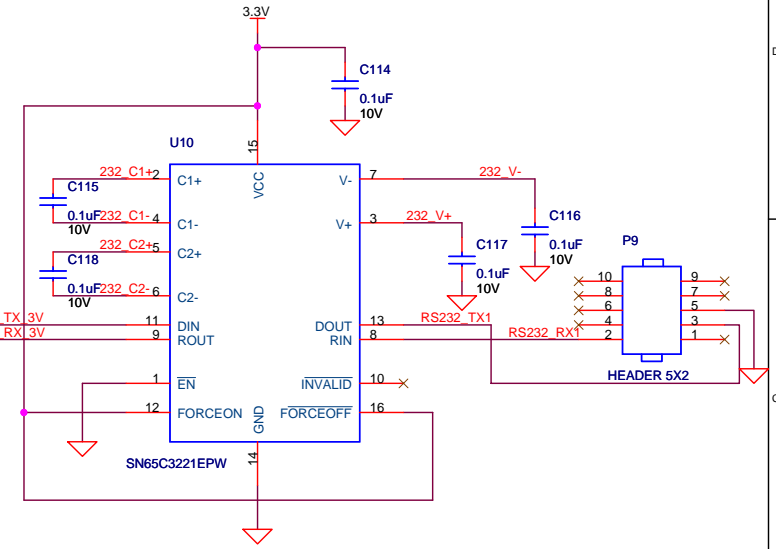
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Beagle - USB Host and Expansion Connector		
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SD/MMC Connector 6 in 1
MMC+, MMCMobile, SD,
MMC, miniSD, RS-MMC



UART3 Serial Port



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